

Balance of optical, structural and electrical properties of textured liquid phase crystallized Si solar cells

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Liquid phase crystallized Si thin-film solar cells on nanoimprint textured glass substrates exhibiting two characteristic, but distinct different surface structures are presented. The impact of the substrate texture on light absorption, the structural Si material properties and the resulting solar cell performance is analyzed. A pronounced periodic substrate texture with a vertical feature size of about 1 μm enables excellent light scattering and light trapping. However, it also gives rise to an enhanced Si crystal defect formation deteriorating the solar cell performance. In contrast, a random pattern with a low surface roughness of 45 nm allows for the growth of Si thin films being comparable to Si layers on planar reference substrates. Amorphous Si/crystalline Si heterojunction solar cells fabricated on the low-roughness texture exhibit a maximum open circuit voltage of 616 mV and internal quantum efficiency peak values exceeding 90% resulting in an efficiency potential of 13.2 %. This demonstrates that high quality crystalline Si thin films can be realized on nanoimprint patterned glass substrates by liquid phase crystallization inspiring the implementation of tailor-made nanophotonic light harvesting concepts into future liquid phase crystallized Si thin film solar cells on glass.

I. INTRODUCTION

The present-day photovoltaic market is dominated by solar cell devices based on multicrystalline Si wafers. Liquid phase crystallization (LPC) of Si thin films on glass represents a promising alternative technology as it allows reducing the Si usage by more than a factor of 10. Just recently, 10 micrometer thin LPC Si solar cells were realized yielding open circuit voltages up to 656 mV, being comparable with the open circuit voltage of record multicrystalline Si wafer devices.^{1,2} So far, the maximum conversion efficiency of LPC Si solar cells is limited to 11.8 %.^{3,4} Incomplete light absorption has been identified as one major efficiency-limiting factor. Dore *et al.* have addressed this issue by randomly texturing the rear side of the Si absorber.⁵ Nevertheless, in order to increase efficiencies towards 16 % and beyond, a light harvesting concept has to be implemented that features both, a patterned front and patterned rear side, reducing the reflectivity and providing light trapping inside the Si absorber layer. Inspired by amorphous or solid phase crystallized Si thin-film solar cell technologies commonly using structured substrates for light trapping^{6,7}, we recently presented a process to realize double-sided textured LPC Si thin-films on nanoimprint patterned glass substrates.⁸ This approach enables a systematic tailoring of the topography of the Si thin film by adjusting the nanoimprint template which can be designed in nearly arbitrary geometry. A high optical

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absorption approaching the lambertian limit was reached. However, the resulting solar cell efficiency was limited to 8.1 % with a corresponding open circuit voltage of 553 mV indicating a deterioration of the electrical absorber quality caused by the high aspect ratio substrate pattern used in this case. Therefore, an in-depth understanding on the impact of a certain substrate texture on the LPC process and the resulting Si material quality is indispensable.

Here in this paper, we present a detailed analysis of the correlation of the substrate texture and the resulting optical, structural and electronic material quality of LPC Si thin films. We used a focussed electron beam for LPC of Si thin films on a high aspect ratio periodic texture, a low surface roughness random texture and a planar glass substrate as model systems. For this purpose we characterized the structural Si material quality by cross-sectional TEM and defect etching and connected it to the electrical material properties, which were assessed by characterizing amorphous Si/crystalline Si heterojunction solar cells. At the same time we investigated the optical absorption in order to evaluate the light harvesting performance. Doing so the interplay of the optical, structural and electrical material properties was analyzed providing design rules for the growth of high quality Si on glass.

II. METHODS AND MATERIALS

A. Realization of nanoimprint patterned substrates

10 x 5 cm² Corning Eagle XG glasses (1.1 mm thick) were cleaned using an alkaline based general purpose cleaner (Mucasol) and dip-coated with an UV-curable alkoxy silane based hybrid-polymer sol-gel solution.⁹ Structuring of the sol-gel layer was performed by UV-nanoimprint lithography using polydimethylsiloxane (PDMS) molds that have been fabricated by casting PDMS solution over a master structure. After the imprinting process UV and thermal curing (500°C, 1h) were applied to achieve crosslinking, decompose the organic residues and densify the sol-gel network resulting in a high temperature stable structured glassy surface. Two different types of substrate textures were realized: (1) A 2 μm periodic u-shaped square lattice pattern with a valley-to-peak height of 1.0 – 1.3 μm and a feature width of 0.6 μm (at half height). This texture is depicted in Figure 1 (a) and will in the following be referred to as the pronounced texture. The respective master consisting of a nickel shim had been fabricated at Fraunhofer ISE, Germany, using interference lithography. (2) A random texture exhibiting a root mean square roughness R_{rms} of 45 nm and characteristic lateral crater dimensions of 1-2 μm. This texture is illustrated in Figure 1 (d) and will be termed the moderate texture. Here, the respective PDMS mold was fabricated using a commercially available textured ZnO:Al layer on glass (master structure), which had been deposited by magnetron sputtering and textured by hydrochloric acid.¹⁰ The master structure exhibited a R_{rms} of 125 nm. The large differences of the R_{rms} of the imprinted substrate texture and of the master structure can be attributed to shrinking of the sol-gel resist during the thermal curing.

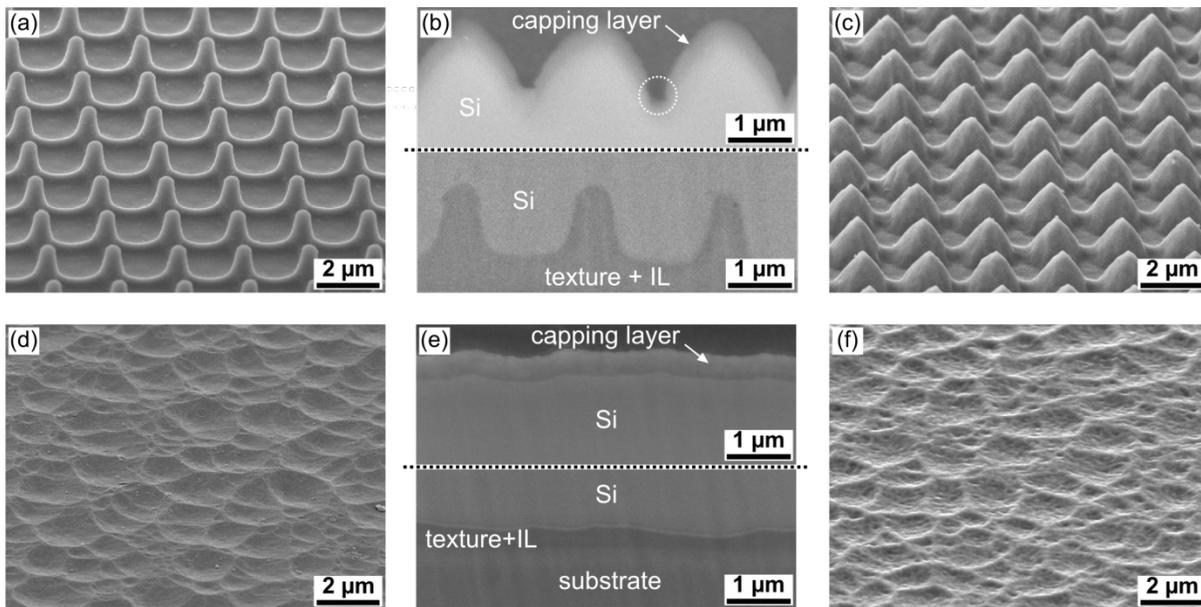


FIG. 1. SEM micrographs of the pronounced periodic (a) and the moderate random sol-gel texture (d). Cross-sectional (b, e) and top-view SEM images (c, f) of 10 μm thick liquid phase crystallized Si thin films prepared on the respective texture.

B. Preparation of the absorber layer

Textured and planar substrates were coated with a multifunctional intermediate layer stack using reactive magnetron sputtering. This layer stack consisted of 200-300 nm SiO_x , 80 nm SiN_x and 10-20 nm SiO_x acting as diffusion barrier, antireflection coating and front side passivation respectively. Subsequently 10 μm of nominally intrinsic nanocrystalline (nc) Si were deposited by e-beam evaporation at a rate between 500 nm/min and 600 nm/min.¹¹ As a dopant source a 2-3 nm thin layer of n-doped ($1 \times 10^{20} \text{cm}^{-3}$) hydrogenated amorphous silicon (a-Si:H) was deposited on top of the nc-Si using plasma enhanced chemical vapor deposition and annealed at 600 $^\circ\text{C}$ for 6 h under nitrogen atmosphere in order to remove hydrogen. Subsequently a 300-500 nm thick SiO_x capping layer was deposited on top of the layer stack by reactive magnetron sputtering. The capping layer fulfils two functions: (1) It inhibits dewetting of the liquid Si thin film due the relaxation of the wetting angle constraints.^{12,13} (2) It prevents the surface of the Si melt from levelling out and thus allows maintaining the initial topography of the as deposited layer.⁸ Due to the directional characteristics of magnetron sputter deposition both the intermediate layers and the capping layer are non-conformal. At the steep flanks of the pronounced substrate texture e. g. the intermediate layer thickness is reduced by about 20% compared to the layer thickness on top and in between the sol-gel features. For the pronounced texture vertical voids inside the capping layer at the v-shaped grooves between two adjacent Si domes can be observed prior to the crystallization process (not shown here). At some of these spots a partial ablation of the capping layer during the crystallization process takes place (see circle in Figure 1 (b)). In the next step the Si is liquid phase crystallized using a line focussed e-beam at a constant scanning speed of 6 mm/s and an energy of around 1 J/mm^2 resulting in lateral crystal growth.¹⁴ No dependence of the crystallization parameters on the type of substrate has been observed. The

dopant concentration of the Si absorber layer was $2 \times 10^{17} \text{ cm}^{-3}$. To reduce internal stress rapid thermal annealing with a 60 s plateau at 950 °C was applied after the LPC process. Subsequently the SiO_x capping layer and defect-rich Si material at the surface stemming from the LPC process were removed using a buffered oxide and HNO₃/H₃PO₄-based etchant. Afterwards a hydrogen plasma treatment at 600°C and 1,0 mbar was applied for 15 min followed by a second Si etching process in order to remove plasma damaged material.^{13,15} Subsequently, a RCA standard cleaning procedure was conducted. pn-heterojunctions were formed by depositing a 5 nm thick intrinsic hydrogenated amorphous Si (a-Si:H) buffer and a 10 nm thick p-doped a-Si:H emitter layer. Figure 1 depicts cross-sectional SEM micrographs of the surface and interface area of the as crystallized layer stack on the pronounced (b) and the moderate substrate texture (e). Figure 1 (c) and (f) show the respective Si surface after the SiO_x capping layer has been removed.

C. Device fabrication

Based on this layer stacks we prepared solar cell devices. In the case of the moderate substrate texture and the planar reference substrate we prepared test devices using a very simple and quick fabrication process featuring an 8 mm x 8 mm large indium tin oxide (ITO) emitter contact pad and a Ti/Pd/Ag absorber contact placed outside the ITO coated emitter area. In the case of the pronounced texture we fabricated back contacted silicon heterojunction solar cells with an area of 10 mm x 6 mm, using an ITO emitter contact layer and Ti/Pd/Ag emitter and absorber contact grids. Finally a 1 mm thick layer of white paint (Tipp-Ex® ECOLUTIONS –Aqua) was applied on top of the layer stack acting as a diffusive rear reflector. For further details on the device structures and the respective preparation processes the reader is referred to the work of Haschke *et al.*^{1,16}

D. Characterization

Absorption and reflection spectra were measured with a LAMBDA 1050 Perkin Elmer UV/VIS/NIR spectrometer. The samples were mounted in the centre of a 150 mm integrating sphere, tilted by 8° with respect to the probe beam and illuminated from the glass side.

In order to investigate the absorption enhancement due to the surface patterning without having to consider parasitic absorption layer stacks without the a-Si:H(i) buffer, the a-Si:H(p) emitter and without contact layers were used for optical characterization. Furthermore a thin diffusive BaSO₄ rear reflector (OptoPolymer) exhibiting low absorption (< 2 %) was applied on these samples by spray coating.

For reflection measurements a cardboard coated with black paint was attached to the rear side of the sample, absorbing the transmitted light. Hereby only the reflected light reaches the detector. For microscopical analysis a Carl Zeiss Libra 200FE transmission electron microscope (TEM) and a Hitachi cold field emitter scanning electron microscope (SEM) in

combination with an EDAX/TSL electron backscatter diffraction (EBSD) setup were used. 30 nm to 40 nm thick TEM lamellae were prepared by mechanical polishing and ion milling. The external quantum efficiency was measured with a custom-made setup, exhibiting a probe beam with a spot size of 3 mm x 2 mm. SunsV_{oc} measurements were conducted at room temperature using a SunsV_{oc} setup from Sinton Instruments with a WCT-100 photoconductance lifetime tool. For the determination of the optical properties one sample of each type was analyzed. For the characterization of the device parameters multiple samples were measured.

III. RESULTS

A. Optical properties

Figure 2 depicts the absorption A and reflection R of 10 μm thick liquid phase crystallized Si thin films on the moderate substrate texture, the pronounced substrate texture and on a planar substrate. As a reference an approximation of the lambertian limit A_{lamb} for a 10 μm thick Si layer is shown assuming zero front side reflection and an ideal rear reflector.¹⁷ While the absorption of the moderately textured Si thin film coincides with the absorption of the planar Si thin film for wavelengths below 700 nm, a moderate, but significant increase compared to the planar reference can be observed for wavelengths above 800 nm. In contrast, the Si layer on the pronounced substrate texture exhibits a very strong absorption enhancement in the long wavelength range above 600 nm and in addition also an increase of absorption in the short wavelength range below 450 nm. In the case of the moderate substrate texture no impact of the structured surface on the antireflection properties can be observed. The absorption enhancement can mainly be attributed to enhanced internal reflection. In the case of the pronounced texture, however, the absorption enhancement is due to both a reduced front side reflectivity and increased internal reflection. Non-vanishing absorption below the Si bandgap can be attributed to parasitic absorption at and in the white paint rear reflector. The light harvesting performance of the investigated model systems was evaluated by calculating the maximum achievable short circuit current density $J_{\text{sc,max}}$ according to the equation

$$J_{\text{sc,max}} = q \int_{300 \text{ nm}}^{1100 \text{ nm}} \frac{S(\lambda) \cdot A(\lambda) \cdot \lambda}{h \cdot c} d\lambda \quad (1)$$

where q corresponds to the elementary charge, hc/λ to the photon energy, $S(\lambda)$ to the solar irradiance of the AM1.5 global reference spectrum and $A(\lambda)$ to the experimentally determined absorption. The moderate substrate texture yields a $J_{\text{sc,max}}$ of 32.0 mA/cm² and the pronounced texture a $J_{\text{sc,max}}$ of 36.3 mA/cm² corresponding to a 6% and 20% increase respectively compared to the planar reference which yields a $J_{\text{sc,max}}$ of 30.2 mA/cm². Using the lambertian limit a $J_{\text{sc,max}}$ of 39.7 mA/cm² is obtained. When comparing the light harvesting performance the presented Si thin films to the lambertian limit it has to be taken into account that roughly 4% of the incoming light are reflected at the air/glass interface and thus the light harvesting properties are underestimated. This holds especially for the short wavelength range between 300 nm and 600 nm where the

film absorption is mainly governed by the reflection properties. However, regarding a future optimized device reflection at the air/glass interface losses can easily be eliminated using standard broadband antireflection coatings.

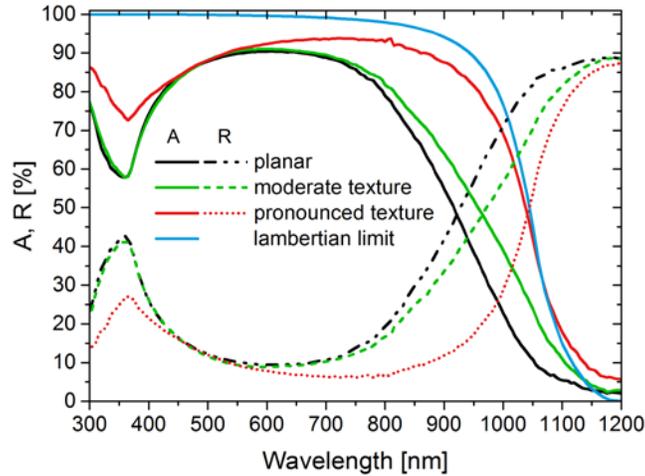


FIG. 2. Optical absorption A and reflection R of 10 μm thick liquid phase crystallized Si thin films on a planar glass (black), the moderate random substrate texture (green) and the pronounced periodic substrate texture on glass (red) along with the corresponding lambertian limit (blue). All samples exhibited a SiN_x antireflection coating between the substrate and the Si thin film and a dispersive BaSO_4 white paint rear reflector.

B. Structural properties

When evaluating Si thin films for an application as absorber layers in solar cells it is indispensable to consider both the optical properties and the structural and electrical material quality. The structural material quality was investigated by EBSD surface orientation maps, cross-sectional TEM analysis and defect etching of the as crystallized Si surface using a Secco etch solution.¹⁸

Figure 3 shows typical EBSD surface orientation mappings of Si thin films with a $\text{SiO}_x/\text{SiN}_x/\text{SiO}_x$ intermediate layer stack on the moderate texture (a), the pronounced texture (b), and on a planar glass substrate (c). The respective images consist of several individual mappings as indicated by the dashed lines. The grain structures of the Si thin films on the moderate texture and on the planar substrate resemble each other exhibiting elongated grains of several millimetres in length. In contrast, in the case of the Si thin film on the pronounced texture agglomerations of grains with reduced sizes in between large elongated grains can be observed.

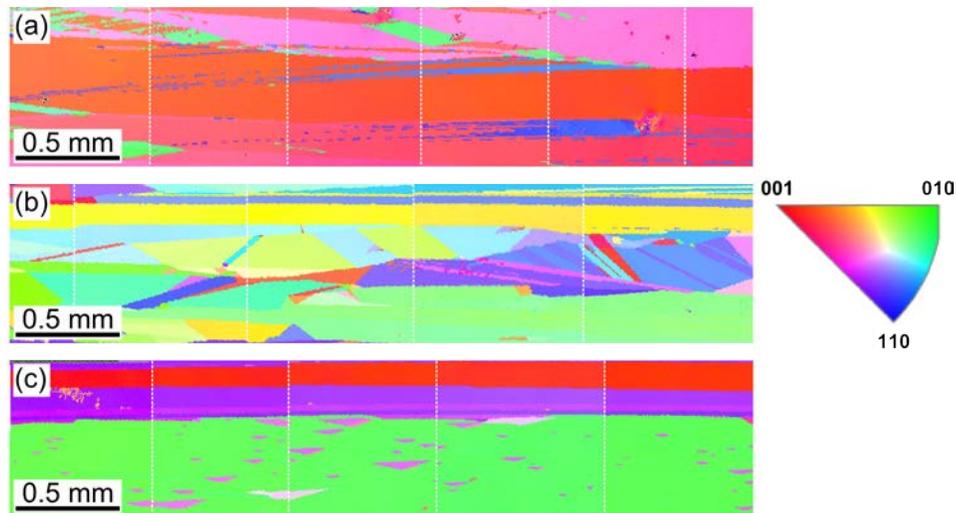


FIG. 3. EBSD surface orientation maps of LPC Si thin films on the moderate substrate texture (a), the pronounced substrate texture (b) and on a planar substrate (c). The color coding of the different crystallographic orientations is depicted on the right hand side.

Figure 4 depicts representative TEM micrographs of the interface area of LPC Si thin films on the pronounced texture with a 50 nm thick SiO_x intermediate layer (a) and on the moderate texture with the above described $\text{SiO}_x/\text{SiN}_x/\text{SiO}_x$ intermediate layer stack (c). In the case of the pronounced substrate texture multiple diffraction features which can be attributed to dislocations in the Si crystal can be observed close to the interface (red circles). A close-up view (green rectangle) of two dislocation related diffraction lines, identifiable by their characteristic well defined and a diffuse edges, is depicted on the right hand side of Figure 4 (a). On the contrary, TEM micrographs of the Si layer on the moderate texture exhibit no dislocation contrasts. Figure 4 (b) and (d) show typical SEM micrographs of the defect etched surface of LPC Si thin-films on the pronounced and the moderate texture respectively. In the case of the pronounced substrate texture all examined spots of the sample exhibited agglomerations of etch pits. They are preferentially located on top of the respective Si domes (red circles). SEM analysis of the defect etched Si thin film on the moderate texture mainly yields etch pit-free areas being comparable to the surface of a planar reference layer (not shown here). The defect-etched line-feature in Figure 4 (d) corresponds to a grain boundary and thus demonstrates that the etching process itself has been performed successfully. Hence, both the TEM and the defect etch analysis show that the density of structural defects is significantly higher for the pronounced texture than for the moderate one.

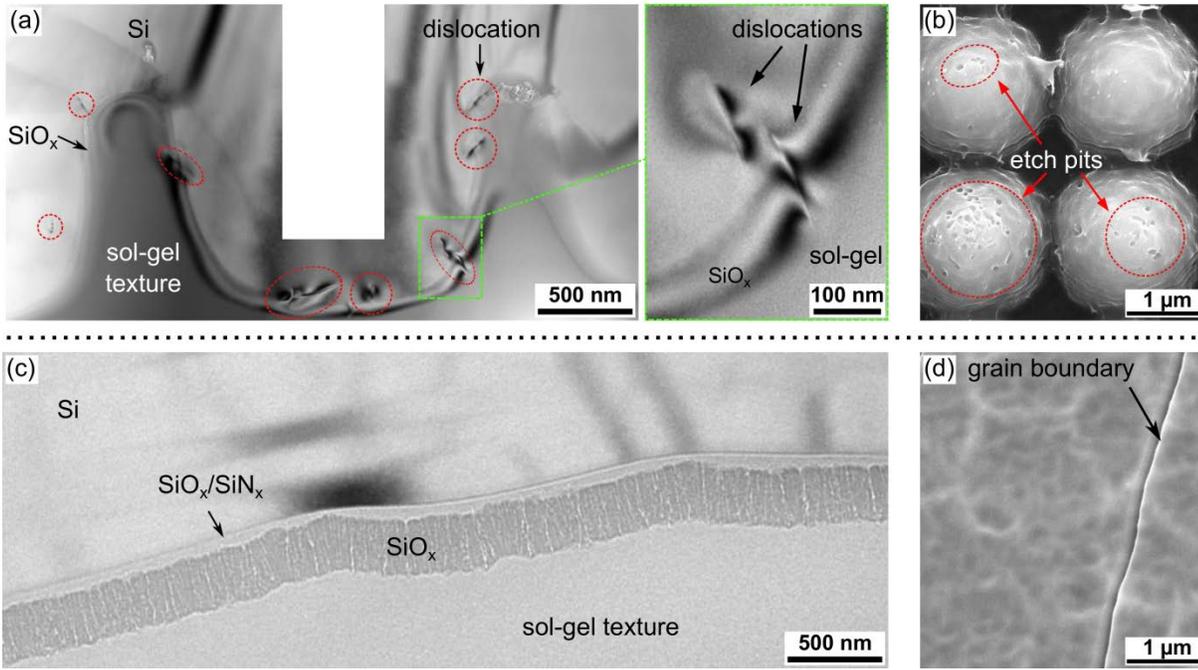


FIG. 4. TEM micrographs of interface area of LPC Si thin films on the pronounced periodic texture with a 50 nm thick SiO_x diffusion barrier (a) and on the moderate random texture with $\text{SiO}_x/\text{SiN}_x/\text{SiO}_x$ intermediate layer stack (c). The SEM micrographs (b) and (d) depict the respective defect etched Si surface.

C. Electrical properties

Figure 5 depicts the external quantum efficiency EQE of solar cell devices fabricated on the pronounced and the moderate texture and on a planar substrate along with the respective absorption A . Figure 6 shows the corresponding internal quantum efficiency IQE_A which has been determined by normalizing the EQE by the respective absorption. Parasitic absorption due to the ITO contact layer, the rear reflector and in the case of the pronounced texture also the metal contact grid leads to an overestimation of the IQE_A for $\lambda > 650$ nm becoming more pronounced as the wavelength increases (dotted lines). For $\lambda < 650$ nm parasitic absorption on the rear side of the solar cell can be neglected. Hence, in this wavelength range the IQE_A represents a measure of the electrical material properties including bulk as well as surface recombination processes.

Inspecting the EQE of the solar cell on the pronounced texture we observe a strong decrease compared to the EQE of the solar cell on the planar substrate (up to a factor of 4), even though the respective absorption properties are superior throughout most of the spectral range. In contrast the EQE of the solar cell on the moderate texture coincides with the EQE of the planar reference cell up to wavelengths of about 850 nm and exceeds it above this wavelength. The latter can be attributed to the increased absorption due the surface texturing. Comparable internal quantum efficiency peak values of around 90 % are obtained for both the solar cell on the moderate substrate texture and the planar substrate, reflecting a high electrical material quality. In contrast the IQE_A of the solar cell device on the pronounced substrate texture proceeds on a very low level (< 28 % for $\lambda < 650$ nm) indicating an enhanced carrier recombination.

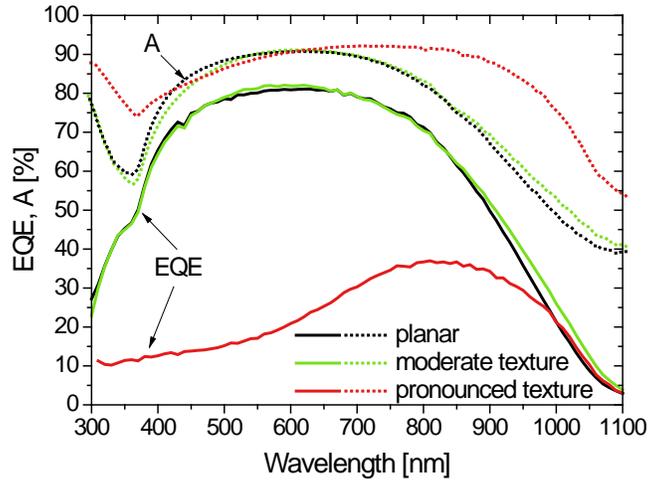


FIG. 5. Absorption (A) and external quantum efficiency (EQE) of solar cell devices fabricated on planar glass, the moderate and the pronounced substrate texture.

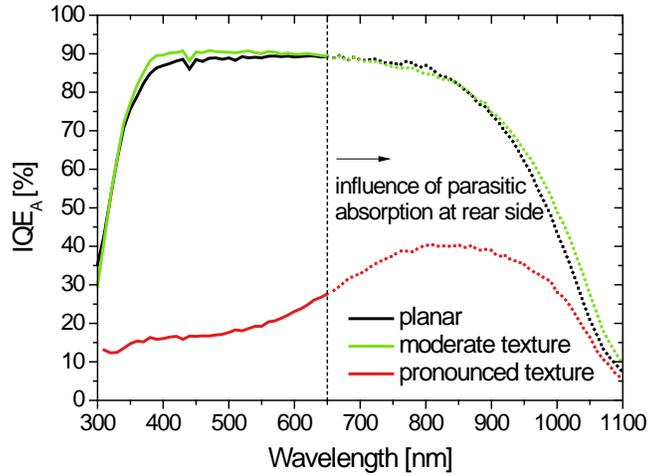


FIG. 6. Internal quantum efficiency $IQE_A = EQE/A$ of solar cell devices fabricated on planar glass, the moderate and the pronounced substrate texture. The dotted lines indicate the wavelength range in which the IQE_A values are overestimated due to parasitic absorption.

Table I lists the maximum and average values along with the respective standard deviation of the open circuit voltage V_{oc} , the short circuit current density J_{sc} , and the pseudo fill factor pFF determined by $SuncV_{oc}$ and EQE measurements respectively. In addition the resulting efficiency potential η_{pot} is tabulated which has been determined by taking the respective maximum values of the V_{oc} , J_{sc} , and pFF into account. The number of available measurements taken into account for the calculation of the respective the average value und standard deviation is listed in brackets in Table I. While the V_{oc} values obtained on the moderate texture, with a maximum of 616 mV, are on the same level as on planar reference substrates, solar cell devices on the pronounced texture exhibit strongly reduced V_{oc} values featuring a maximum V_{oc} of 539 mV. A comparable reduction of the V_{oc} on this type of texture has already been observed in the case of a p-doped Si absorber layer.⁸ Similarly the J_{sc} is significantly lower when performing the LPC process on the pronounced texture: The maximum and average J_{sc} values are

reduced by a factor 2.6 in comparison to solar cells on planar substrates. In the case of the moderate substrate texture the maximum J_{sc} of 27.4 mA/cm² slightly exceeds the respective value on planar glass of 27.0 mA/cm². However, the average J_{sc} values were with 25.2 ± 1.1 mA/cm² (moderate texture) and 25.3 ± 1.2 mA/cm² (planar) on a comparable level. The fact that despite the comparable material quality and the absorption enhancement no significant increase of the J_{sc} can be observed for the solar cell on the moderate substrate texture as opposed to the planar sample can be explained by a slightly higher thickness of the SiN_x antireflection coating in the case of the moderate substrate texture leading to additional reflection losses between 350 nm and 550 nm (see absorption spectra in Figure 5). The resulting efficiency potential η_{pot} amounts to 13.2 % in the case of the moderate substrate texture being slightly higher than the respective value for the planar substrate of 13.0 %, while the efficiency potential of the solar cell device on the pronounced substrate texture is with 4.3 % significantly reduced.

TABLE 1. Average and maximum values and corresponding standard deviation of the open circuit voltage V_{oc} , the short circuit current density J_{sc} , and the pseudo fill factor pFF of solar cell devices realized on the moderate and the pronounced substrate texture and on planar glass. In addition the respective efficiency potential η_{pot} is listed. The number of measurements taken into account is put in parentheses.

Type	$\overline{V_{oc}}$ (mV)	max. V_{oc} (mV)	$\overline{J_{sc}}$ (mA/cm ²)	max. J_{sc} (mA/cm ²)	\overline{pFF} (%)	max. pFF (%)	η_{pot} (%)
Planar	599 ± 12 (6)	615	25.3 ± 1.2 (12)	27,0	72.4 ± 4.7 (6)	78.2	13.0
moderate texture	598 ± 11 (12)	616	25.2 ± 1.1 (16)	27,4	73.7 ± 3.5 (12)	78.2	13.2
pronounced texture	484 ± 52 (4)	539	9.7 ± 0.5 (7)	10,4	65.9 ± 7.0 (4)	76.1	4.3

IV. DISCUSSION

The above presented results reveal that the geometry of the substrate texture not only determines the optics of the resulting structured Si thin film but also strongly impacts the structural and electrical material properties: Si thin films on the pronounced texture provide excellent light harvesting especially in the NIR wavelength range enabling a $J_{sc,max}$ of 36.3 mA/cm² even though the glass surface was missing an antireflection coating. However, the high aspect ratio texture also leads to an increased density of structural defects and a reduced solar cell performance compared to planar reference layers. Vice versa, Si layers on the moderate texture feature a high structural quality and a solar cell performance comparable to Si layers on planar substrates but yield only a slight absorption enhancement. Thus, the substrate textures investigated in this work define a parameter range concerning the respective feature height in which an optimal texture allowing a balance between light harvesting and material quality is to be expected.

The observed degradation of the solar cell parameters in the case of the pronounced substrate texture could be in principle due to both a reduced bulk diffusion length and enhanced recombination at the structured Si/SiO_x interface. The Si surface area enlargement at this interface which roughly amounts to a factor of 1.7 leads to an increased surface recombination current compared to a planar substrate. This can be taken into account by introducing an effective surface recombination velocity S_{eff} according to $S_{eff} = K * S$, where K represent the surface area enlargement and S the surface recombination

velocity.¹⁹ Haschke *et al.* determined an upper limit of the recombination velocity at the Si/SiO_x interface of $S = 500$ cm/s for an n-doped LPC Si thin film on planar glass.¹ 1D numerical simulations using the software AFORS-HET²⁰ were performed to estimate the effect of the surface area enlargement. The results indicate that the severe reduction of the quantum efficiency cannot solely be explained by the surface area enhancement. Considering the enhanced density of structural defects observed in our experiments and the fact that dislocations in Si are known to act as recombination centers²³⁻²⁶, we conclude that a reduction of the diffusion length due to bulk defects is the dominant mechanism in this case.

Poor solar cell performance caused by an increased oxygen incorporation resulting from an enhanced film porosity on the high aspect ratio texture as it was reported by Merkel *et al.* for amorphous Si thin film precursor layers can be ruled out in the case of nc-Si precursors. Secondary ion mass spectroscopy measurements of LPC Si thin films on a SiO_x/SiC_x intermediate layer stack, being comparable to the Si thin films analyzed in this study, yield with 2×10^{17} cm⁻³ for both the pronounced texture and the planar substrate a comparable oxygen concentration.²⁷ The increased density of dislocations observed in the Si thin film on the pronounced texture implies an enhanced thermal stress during the cooling phase after the solidification exceeding the critical resolved shear stress (CRSS) of Si. Lateral crystal growth is expected at low supercooling of the melt of about 50 °C – 200 °C below the melting point of Si.²⁸ In this range the CRSS of solid Si amounts to $2-3 \times 10^7$ dyn/cm².²⁹ The enhanced thermal stress could be explained as follows: As a first approximation the layer stack on the moderate substrate texture can be considered as a planar layer system. For this type of model system thermal stress can build up only parallel to the interface planes or the substrate surface respectively. In direction normal to the layer interface the system can relax. In contrast, for a high aspect ratio structured layer system with nearly vertical sidewalls, as it is the case for the pronounced texture, thermally induced stress states can build up also in the direction normal to the layer interfaces, e.g. in a silicon volume between two adjacent vertical sol-gel features.

The high open circuit voltages of above 600 mV and the IQE_A peak exceeding 90% achieved on the basis of Si thin-films on the moderate texture demonstrate that on nanoimprint patterned sol-gel high quality state of the art LPC Si thin films can be realized. The challenge on the way towards highly efficient LPC Si thin-film solar cells on tailored micro- and nanopatterned surfaces now consists of improving the antireflection and light trapping properties by increasing the surface roughness or structure height respectively and at the same time maintain the excellent Si material quality. To achieve this further work is needed to identify the appropriate parameters of the texture geometry, such as sidewall angle, surface roughness/structure height and filling ratio for optimal crystal growth.

V. SUMMARY

We prepared double side textured crystalline Si thin-films on nanoimprint patterned sol-gel glass substrates by e-beam induced liquid phase crystallization and studied the correlation of the substrate texture with the optical, structural and electrical properties of the respective Si thin-films. As model systems we used a u-shaped square lattice pattern with a valley-to-peak height of 1.0 - 1.3 μm and a pitch of 2 μm as well as a random pattern exhibiting a surface roughness of 45 nm and characteristic lateral dimensions of 1-2 μm . Si thin films on the pronounced periodic texture allow for excellent light trapping, while the respective thin films on the low-surface roughness texture show only a slight absorption enhancement in the near infrared. TEM analysis and defect etching revealed that Si thin-films crystallized on the pronounced periodic texture exhibit an increased density of structural defects compared to Si thin-films on the moderate random texture. Solar cells prepared on the pronounced periodic texture exhibit a reduced V_{oc} and quantum efficiency compared to reference cells on planar glass. In contrast, solar cells realized on the low surface roughness random texture yield state of the art open circuit voltages exceeding 600 mV and internal quantum efficiency peak values of above 90 % being comparable to the respective parameters of planar reference solar cells. The corresponding efficiency potential amounts to 13.2 %. In conclusion, the results of this study demonstrate that liquid phase crystallization on patterned sol-gel/glass substrates is capable of achieving high quality Si thin films for PV applications.

ACKNOWLEDGMENTS

The authors thank C. Klimm, U. Bloeck, M. Wittig, M. Reiche, and P. Plocica for the specimen preparation and SCHOTT AG, Mainz, Germany for funding VP's Ph.D.. The German Federal Ministry of Education and Research is acknowledged for funding the research activities of the Nano-SIPPE group within the program NanoMatFutur (No. 03X5520).

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